

Fig. 1

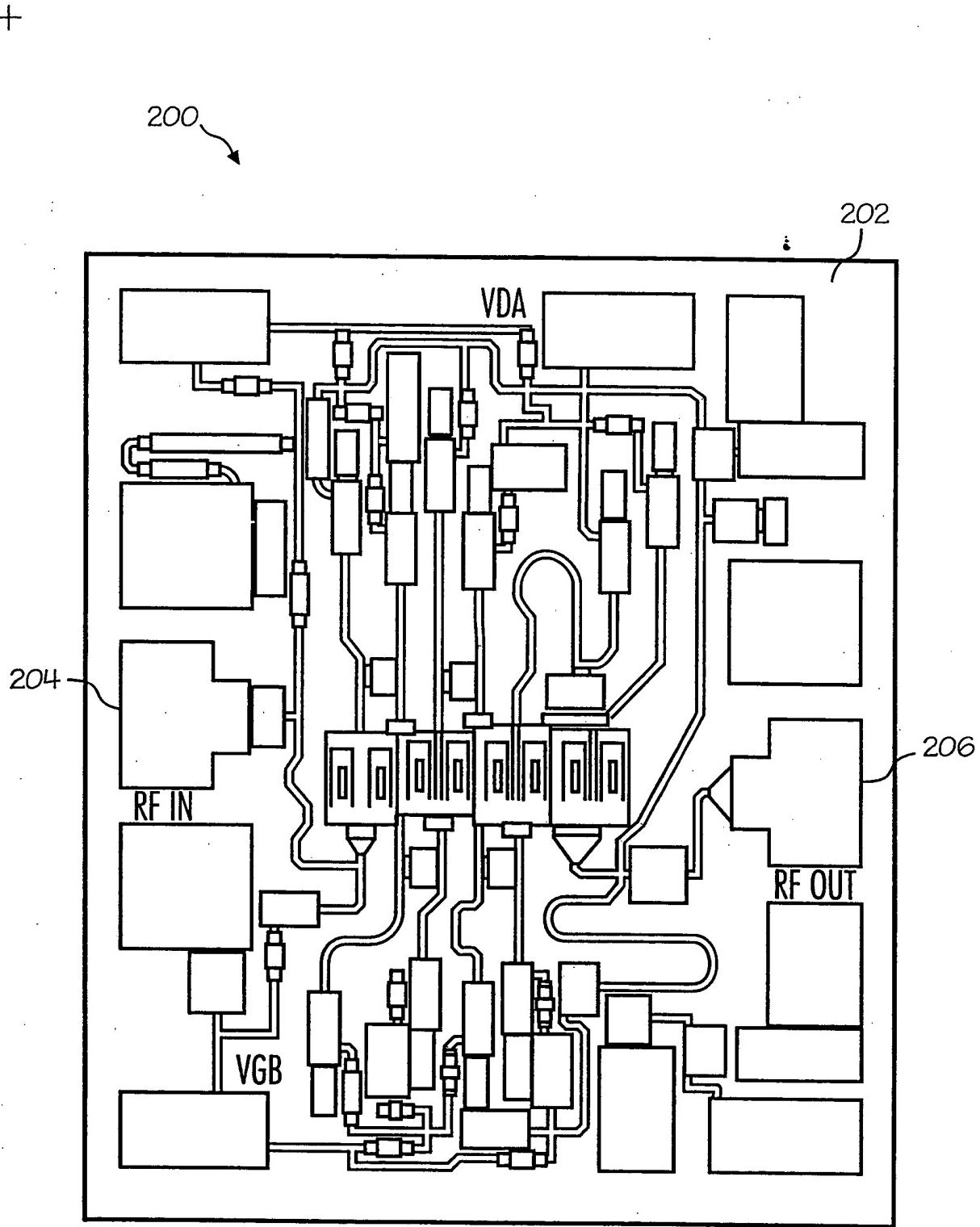


Fig. 2

+

300

304

VD1

VD2

RF OUT

306

RF IN

VG1

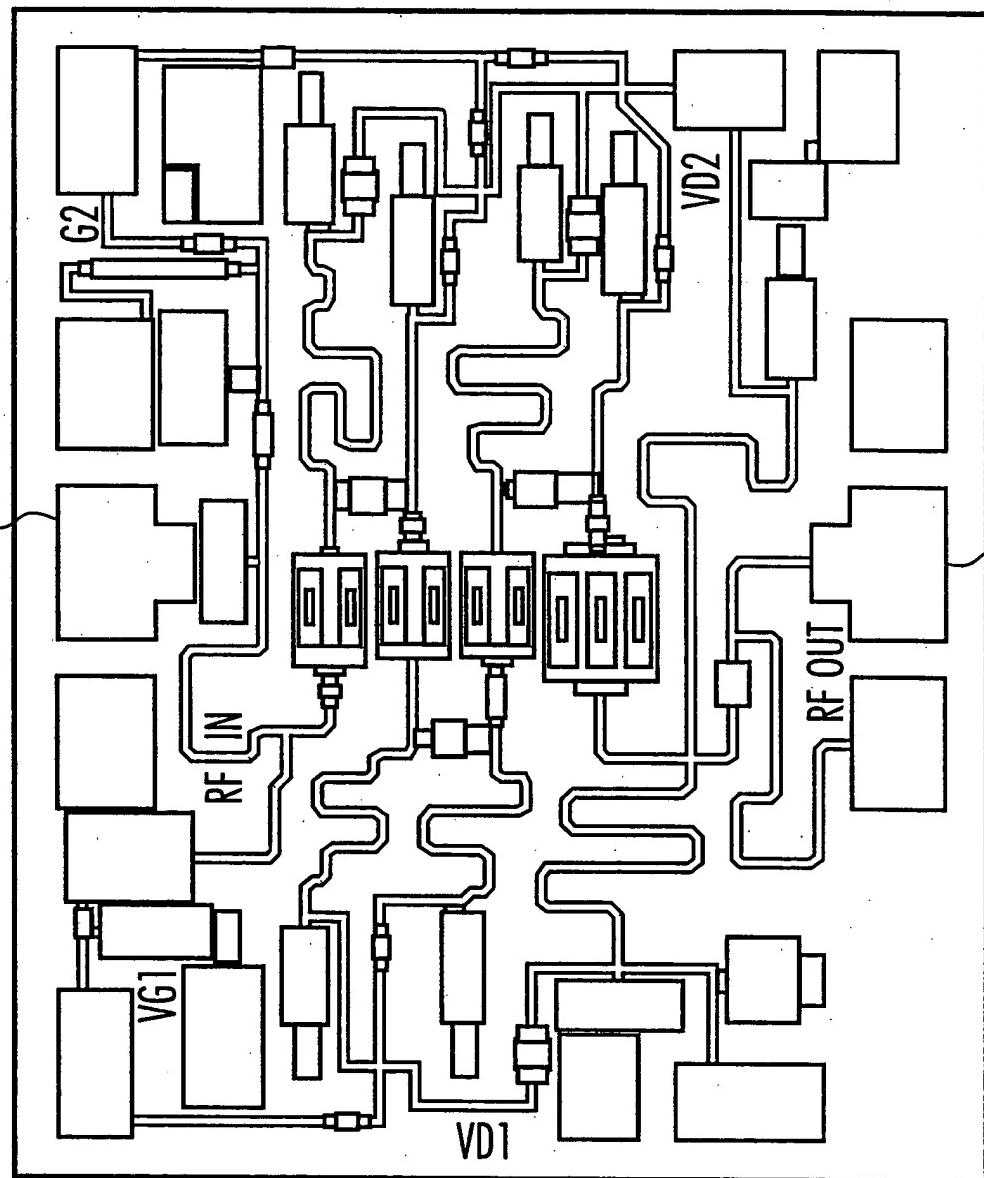


Fig. 3

+

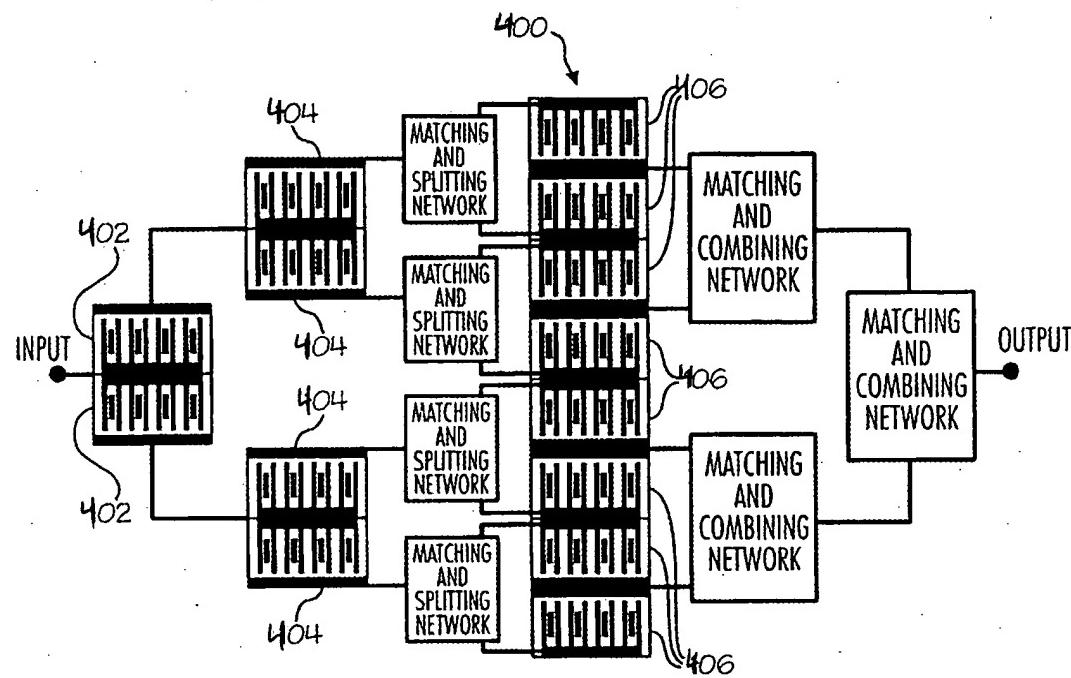


Fig. 4

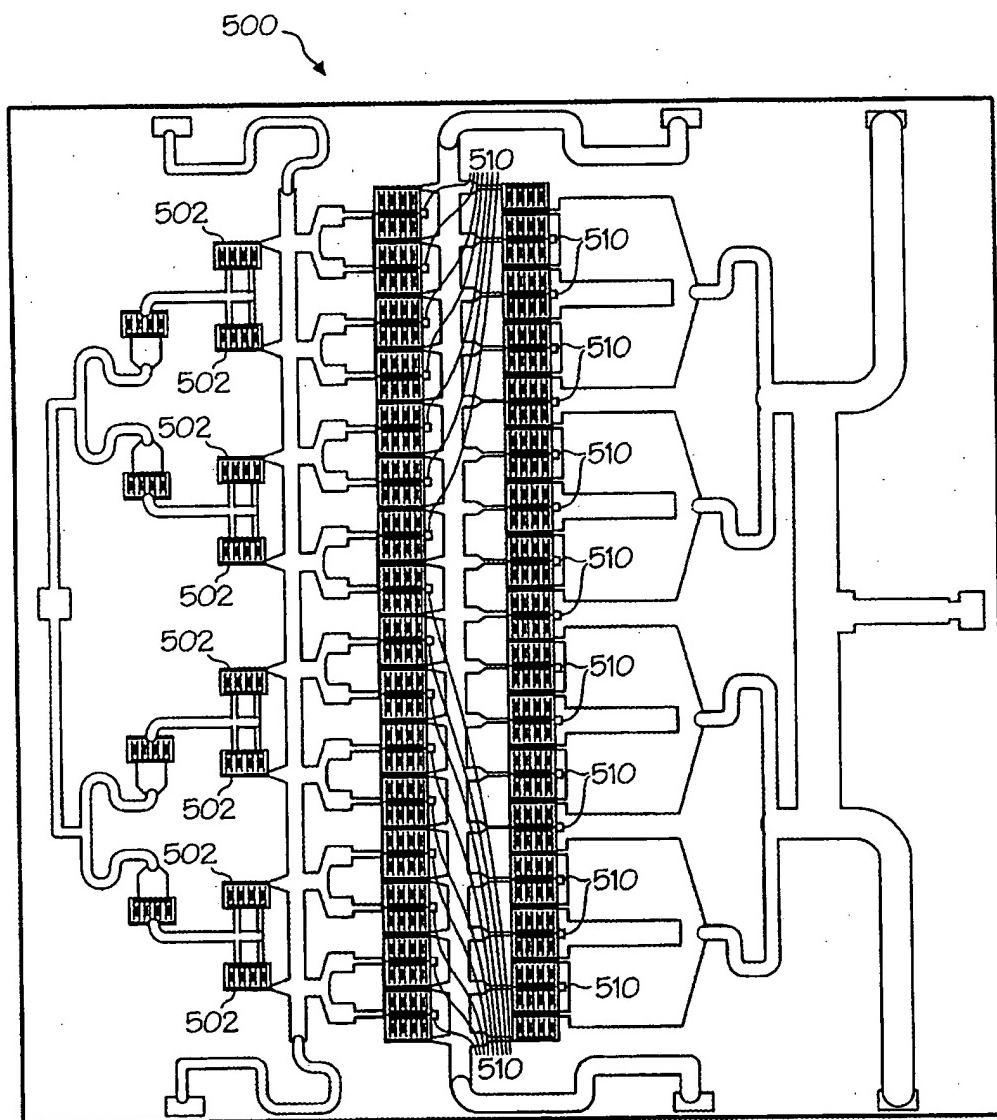


Fig. 5

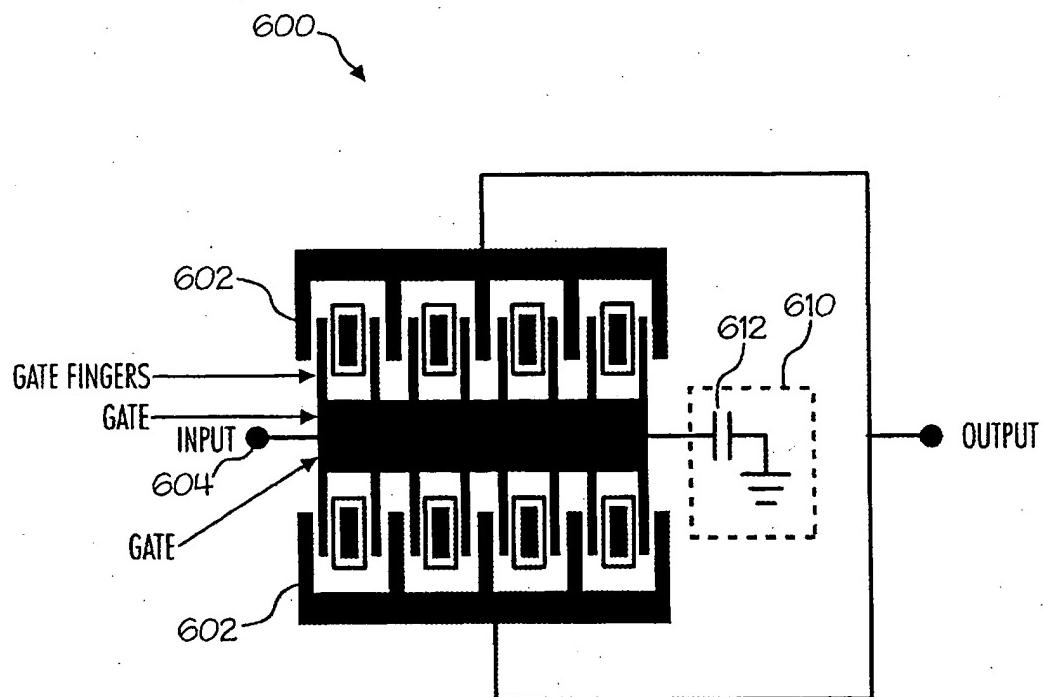


Fig. 6

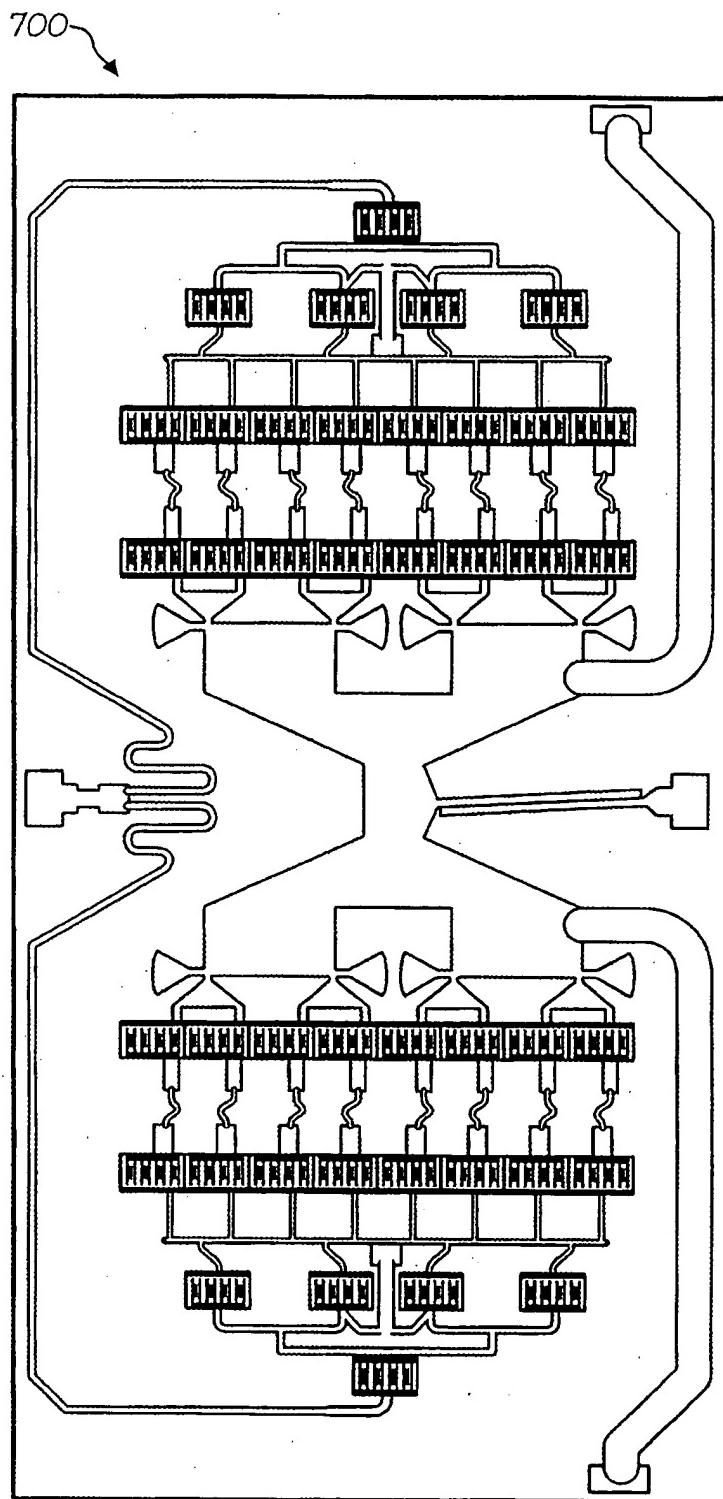


Fig. 7

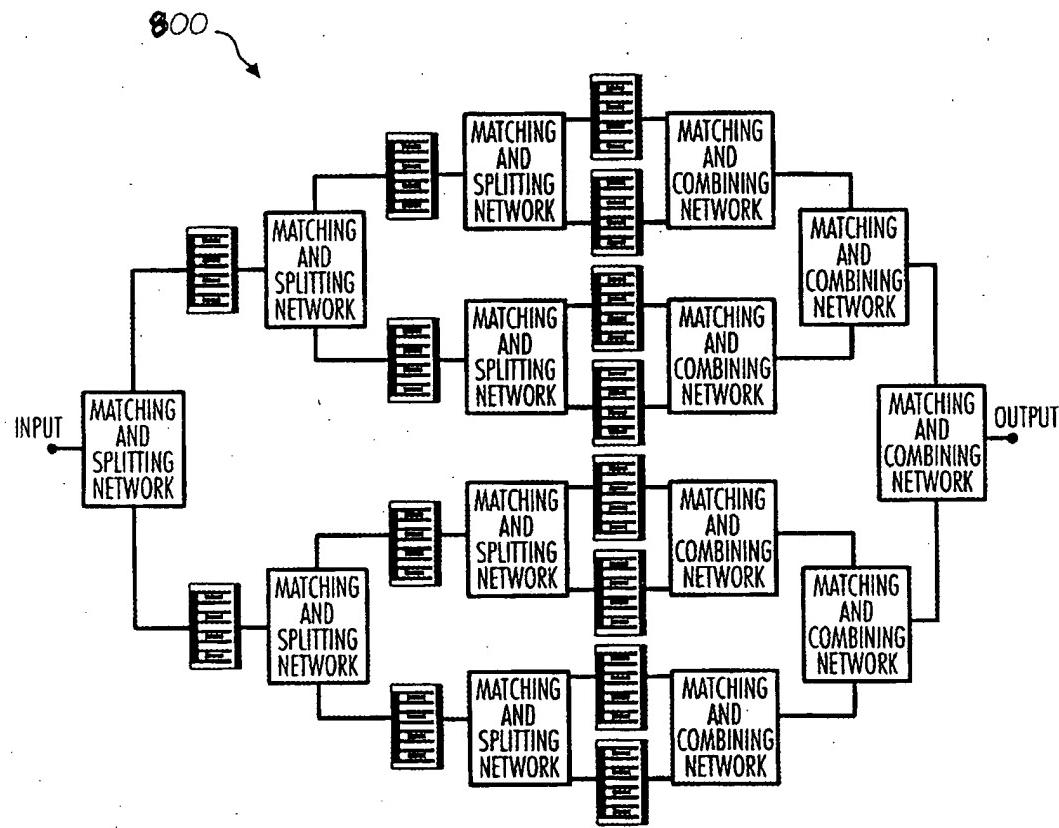


Fig. 8
PRIOR ART

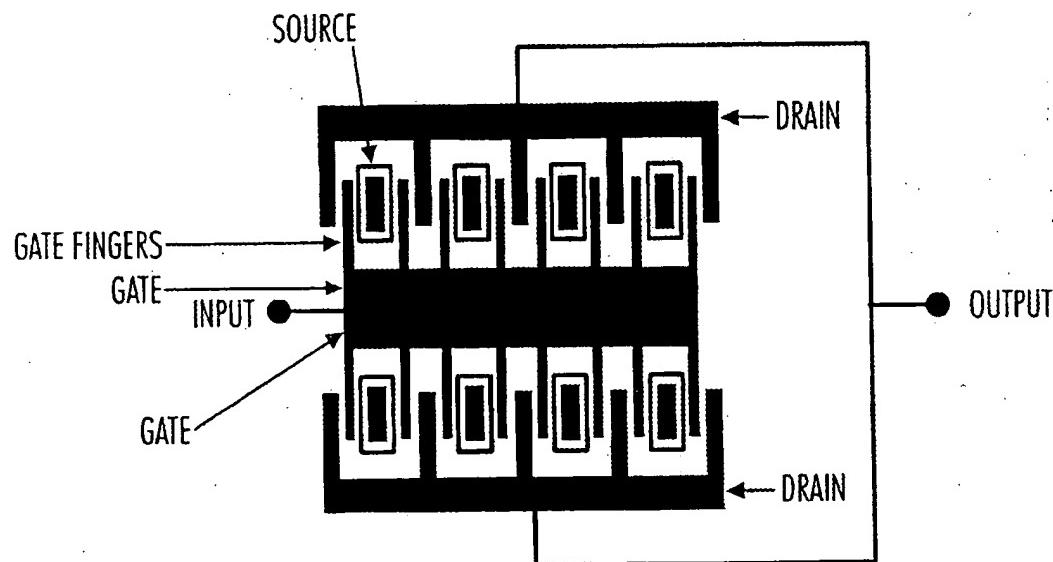


Fig. 9A

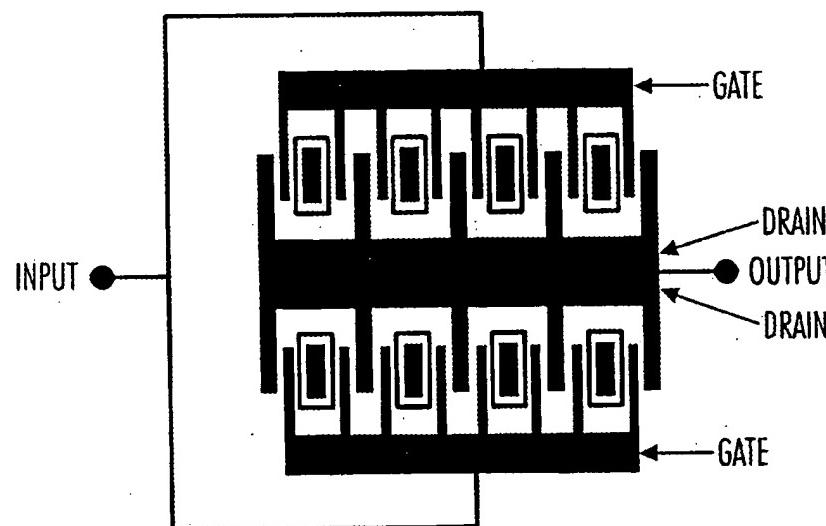


Fig. 9B

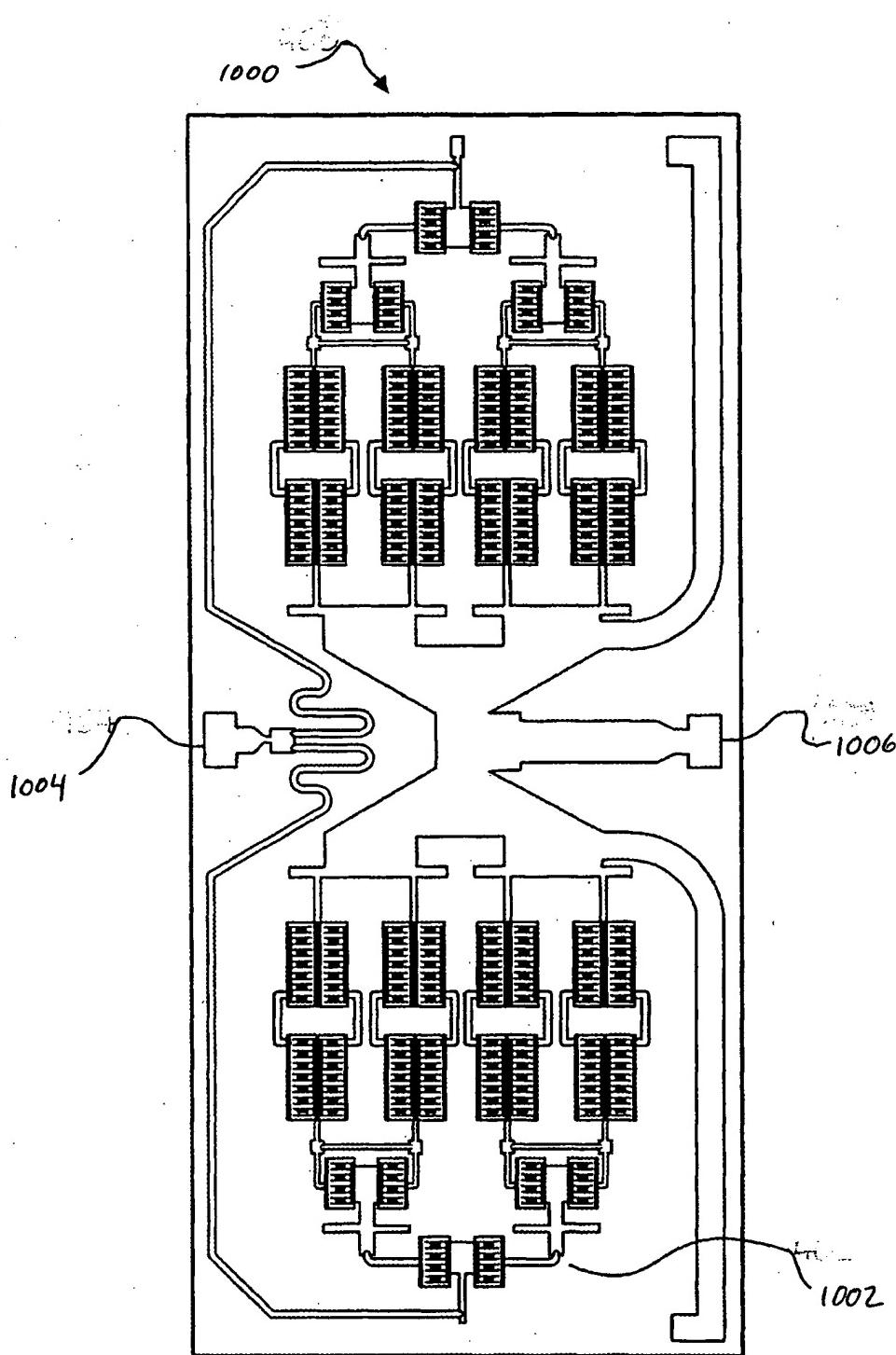


Fig. 10

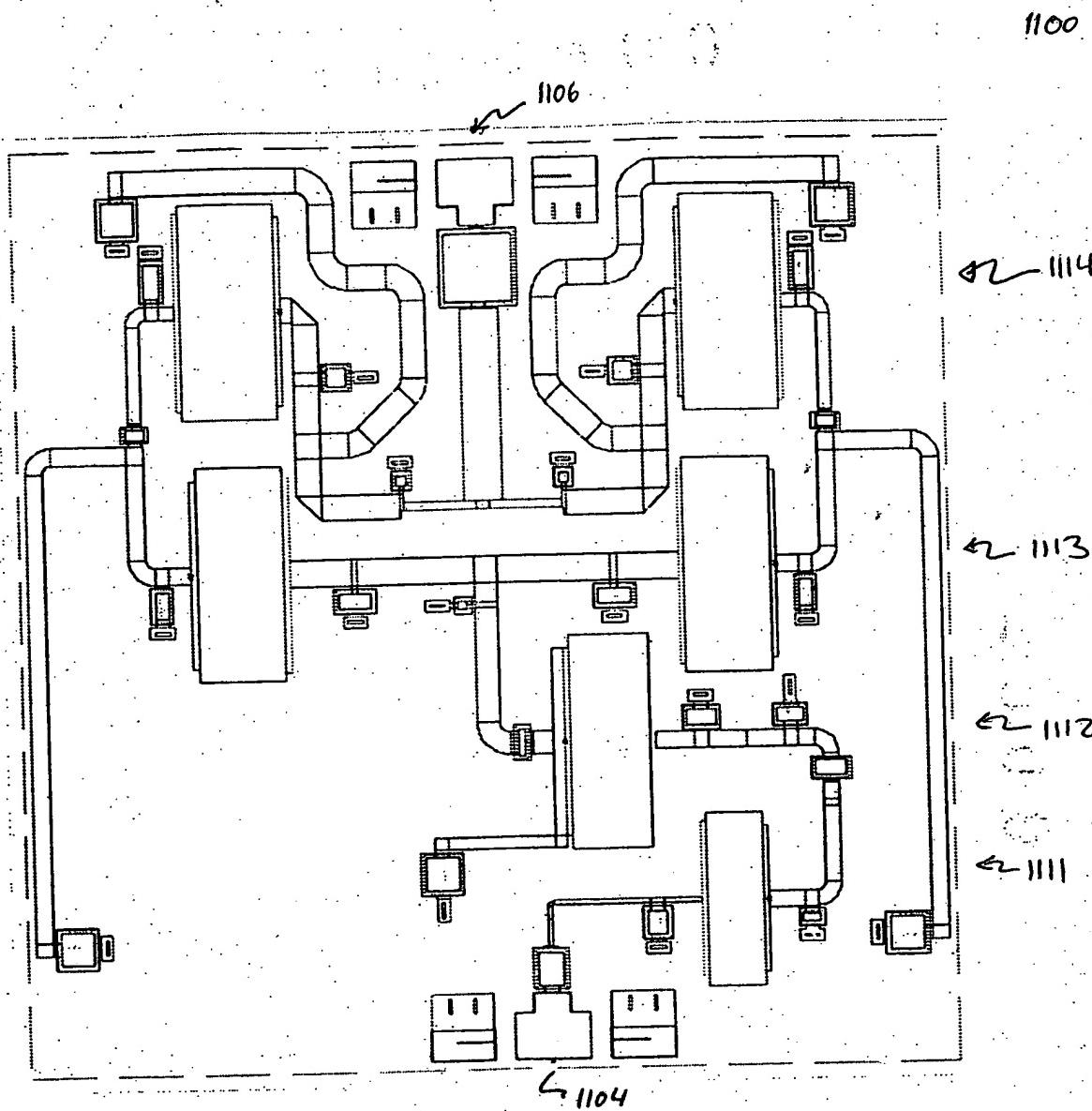


Fig. 11